

L Number	Hits	Search Text	DB	Time stamp
1	2	("4924075").PN.	USPAT; EPO; JPO	2001/09/17 07:11
-	14	235/\$7.ccls. and "fpga"	USPAT; EPO; JPO	2001/09/17 07:11
-	3558	"field programmable gate array" or "FPGA"	USPAT; EPO; JPO	2001/09/14 13:21
-	279	("field programmable gate array" or "FPGA") and "security"	USPAT; EPO; JPO	2001/09/07 10:26
-	2994	("field programmable gate array" or "FPGA") and @ad < "19980901"	USPAT; EPO; JPO	2001/09/14 08:55
-	240	((("field programmable gate array" or "FPGA") and @ad < "19980901") and "security"	USPAT; EPO; JPO	2001/09/14 11:34
-	13	((("field programmable gate array" or "FPGA") and @ad < "19980901") and 235/\$7.ccls.	USPAT; EPO; JPO	2001/09/14 14:18
-	552	235/\$7.ccls. and "security system"	USPAT; EPO; JPO	2001/09/07 10:31
-	1	(235/\$7.ccls. and "security system") and ("field programmable gate array" or "FPGA")	USPAT; EPO; JPO	2001/09/07 10:31
-	8	((("field programmable gate array" or "FPGA") and @ad < "19980901") and 705/\$7.ccls.	USPAT; EPO; JPO	2001/09/07 10:59
-	3002	("field programmable gate array" or "FPGA") and @ad < "19980901"	USPAT; EPO; JPO	2001/09/14 08:55
-	13	((("field programmable gate array" or "FPGA") and @ad < "19980901") and 235/\$7.ccls.	USPAT; EPO; JPO	2001/09/14 08:56
-	724	235/382.ccls.	USPAT; EPO; JPO	2001/09/14 09:02
-	433	235/382.ccls. and "security"	USPAT; EPO; JPO	2001/09/14 09:02
-	8	((("field programmable gate array" or "FPGA") and @ad < "19980901") and 705/\$7.ccls.	USPAT; EPO; JPO	2001/09/14 09:45
-	1	("6089460").PN.	USPAT; EPO; JPO	2001/09/14 09:50
-	1	"5963980".PN.	USPAT	2001/09/14 09:52
-	1	"5898783".PN.	USPAT	2001/09/14 09:52
-	1	"5890189".PN.	USPAT	2001/09/14 09:54
-	1	"5867507".PN.	USPAT	2001/09/14 09:55
-	5	((("5446266") or ("5898783") or ("6003135"))).PN.	USPAT; EPO; JPO	2001/09/14 12:39
-	6	((("5446266") or ("5898783") or ("6003135") or ("6089460"))).PN.	USPAT; EPO; JPO	2001/09/14 12:39
-	3583	"field programmable gate array" or "FPGA"	USPAT; EPO; JPO	2001/09/14 13:25
-	402	("field programmable gate array" or "FPGA") and "compile"\$1	USPAT; EPO; JPO	2001/09/14 14:20
-	46	((("field programmable gate array" or "FPGA") and "compile"\$1) and "C++"	USPAT; EPO; JPO	2001/09/14 13:23
-	0	("field programmable gate array" or "FPGA") and "synthesis tool"\$1	USPAT; EPO; JPO	2001/09/14 13:25
-	384	("field programmable gate array" or "FPGA") and "synthesis"\$	USPAT; EPO; JPO	2001/09/14 13:25
-	193	((("field programmable gate array" or "FPGA") and "synthesis"\$) and "layout"	USPAT; EPO; JPO	2001/09/14 13:25
-	11	((("field programmable gate array" or "FPGA") and "synthesis"\$) and "layout") and "C++"	USPAT; EPO; JPO	2001/09/14 13:27
-	105	((("field programmable gate array" or "FPGA") and "synthesis"\$) and "layout") and "compil"\$7	USPAT; EPO; JPO	2001/09/14 13:30
-	85	((("field programmable gate array" or "FPGA") and "synthesis"\$) and "layout") and "compil"\$7) and @ad < "19980901"	USPAT; EPO; JPO	2001/09/14 13:31
-	13	((("field programmable gate array" or "FPGA") and @ad < "19980901") and 235/\$7.ccls.	USPAT; EPO; JPO	2001/09/14 14:18

-	4	((("field programmable gate array" or "FPGA") and @ad < "19980901") and 235/\$7.ccls.) and "battery"	USPAT; EPO; JPO	2001/09/14 14:18
-	47	((("field programmable gate array" or "FPGA") and "compile"\$1) and "battery"	USPAT; EPO; JPO	2001/09/14 14:27
-	19	((("field programmable gate array" or "FPGA") and "compile"\$1) and "battery") and ("IC" or "smart")	USPAT; EPO; JPO	2001/09/14 14:26
-	3196	235/\$7.ccls. and ("IC card" or "smart card")	USPAT; EPO; JPO	2001/09/14 14:29
-	520	(235/\$7.ccls. and ("IC card" or "smart card")) and "battery"	USPAT; EPO; JPO	2001/09/14 14:30
-	35	(235/\$7.ccls. and ("IC card" or "smart card")) and "battery powered"	USPAT; EPO; JPO	2001/09/14 14:30